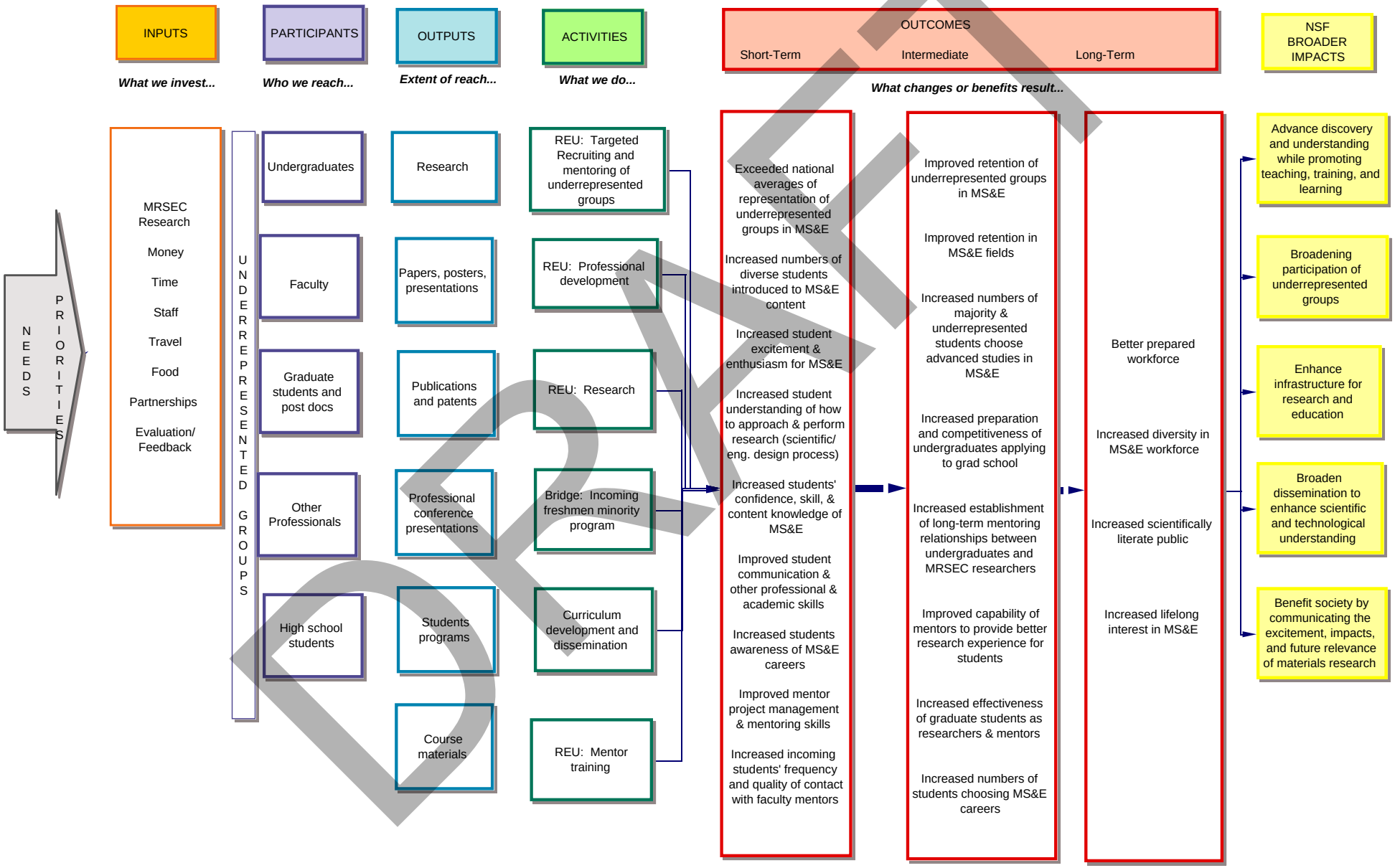
MRSEC E/O LOGIC MODEL

Undergraduate Research Experiences



DRAFT