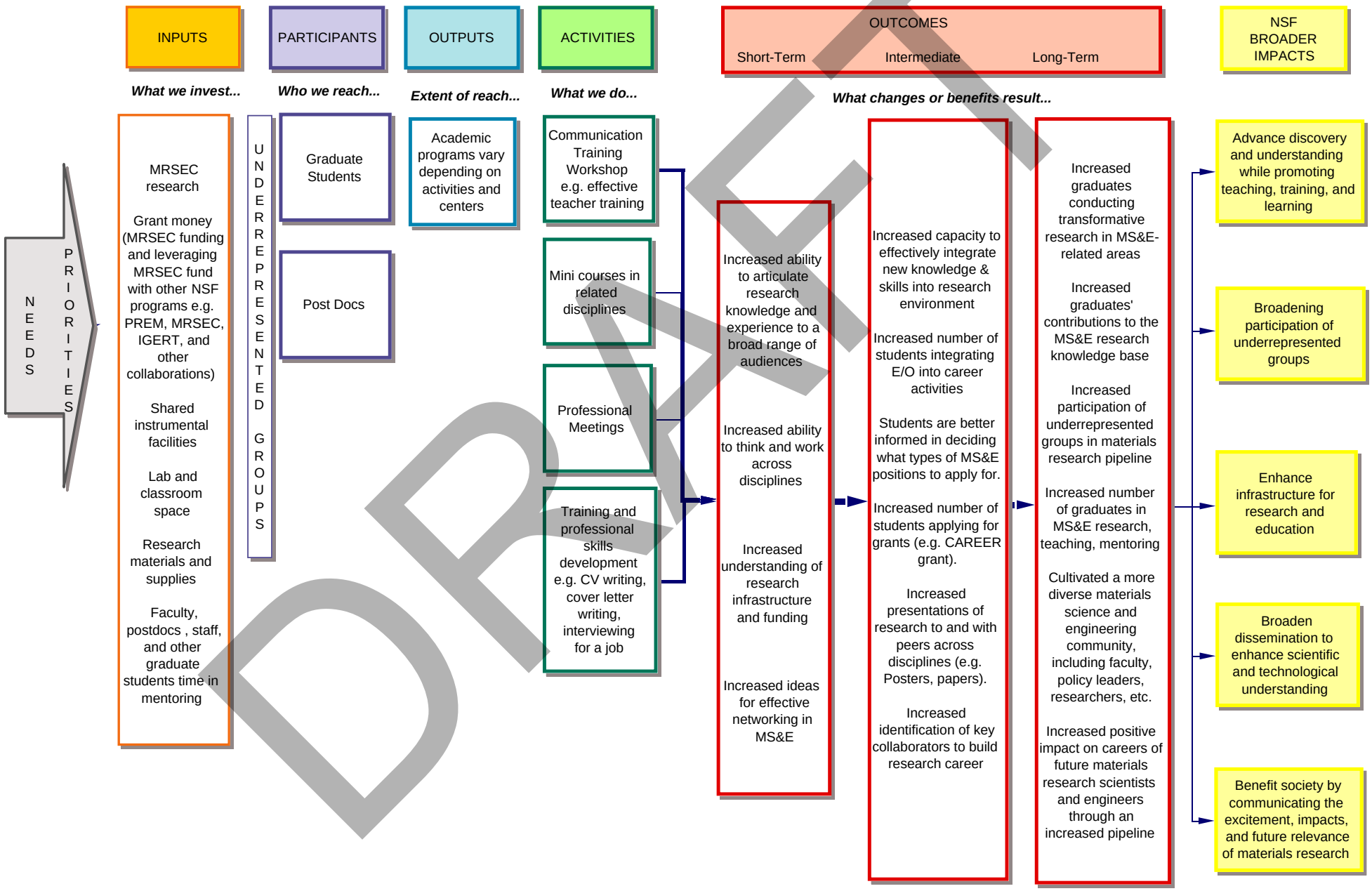
MRSEC E/O LOGIC MODEL

Graduate Student Training



DRAFT